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We are looking for a Formal Verification student

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- Verify hardware RTL designs at pre-silicon early development stages.
- Learn and dive into new technologies, methodologies, and tools.
- Define and develop Formal Verification environments
- Use industry leading edge tools and methodologies
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- BSc/MSc Student for CS / EE / CE or similar
- Completed at least 3 semester and with at least 2 semesters till graduation.
- Ability to work at least 20 hours a week.
- Fast self-learning abilities
- Accurate, precise, focused and able to dive into details
- Independent worker and a team player with high communication skills
- Knowledge in computer logic, mathematic or theory of CS - an advantage

Do you know a student who may qualify for this? Recommend now:

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