



## About Marvell

Marvell first revolutionized the digital storage industry by moving information at speeds never thought possible. Today, that same breakthrough innovation remains at the heart of the company's storage, processing, networking, security and connectivity solutions. With leading intellectual property and deep system-level knowledge, Marvell's semiconductor solutions continue to transform the enterprise, cloud, automotive, industrial and consumer markets.

## Formal Verification student

We are offering you a great opportunity to join the Formal Verification team at the Switch IP department and be a part of the IP development for the next generation Switch.

As a student in the Formal Verification team you'll be responsible for the verification of ASIC/VLSI of a complex IP and for the development of complex Formal Verification structures.

You will be working with Verilog, System Verilog, SVA, Perl, Python  
You will become an expert verification and formal engineer.

Requirements:

- Student in CS/CE/EE, at least 3 semesters left till graduation
- Knowledge of logical gates
- Knowledge in programming
- Good learning skills
- Problems solving skill

A short video about formal verification:

<https://www.youtube.com/watch?v=-CTNS2D-kbY>

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