

Who are we?

Toga Networks is Huawei's R&D center in Israel, and is one of the most influential technology companies around the world. Communication is changing dramatically, faster than ever before. We are here to get ahead of time – and guarantee a better-connected world. Our company is composed of small, focused teams that act as independent thinking groups, looking and seeking future technologies that can serve Huawei customers worldwide.

What is our goal?

Our aim is to create a world based on the most innovative based breakthrough leading technologies solutions by focusing on innovative technologies and deliver breakthrough technologies to Huawei's various business units.

What will you earn?

The opportunity to be a part of highly skilled technology teams, while collaborating with the top architects and experts in the field. You will also enjoy working in a dynamic, flexible and creative environment with nurturing social and family life.

What are we looking for?

Deep learning /Data science SW developer student

Toga's innovative storage solutions employ AI based modules for improving NAND flash storage management.

We are looking for a deep learning/data science student to join the AI team, and support the development and analysis efforts of those modules.

Education: B.Sc. in Electrical Engineering/Computer Science

Requirements:

- Hands on experience in execution and optimization of neural networks.
- Experience in developing using AI frameworks such as Tensor Flow, PyTorch and/or Caffe2. Experience in data pre-processing.
- Experience in HW/SW design is an advantage.
- Knowledge in storage systems (NAND flash, FTL) is an advantage.

Qualifications:

- Good interpersonal skills
- Very good technical skills
- Team player
- Independent and self-learning
- Well arranged in working and coding methods

Please send your CV to inbal.klebanov@toganetworks.com

CPU Core Architect - Fresh Graduate

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JOB DESCRIPTION

Creative Electrical Engineering or Computer Science engineer to participate in the definition of future Digital Signal Processors

REQUIREMENTS

- MsC or PhD graduate
- Computer architecture background - Advantage

- VLSI background – Advantage

Please send your CV to the following e-mail:

Yael.kushnir@toganetworks.com