Real-Time Embedded Firmware Verification and Validation Student

Locations- Israel, Petah-Tikva/ Beer Sheva

Job Details:

Intel's P-Core group is looking for a Firmware (FW) Verification and Validation student to define, design, code (C++, Python), run, and analyze a validation and verification environment, tests, and tools for the innovative core PTP (Power/Thermal/Performance) manager firmware using advanced firmware/software engineering methodologies.
The job also includes modeling the embedded system with its components for functional validation, power evaluation, performance enhancements, and algorithm optimization. The expectation is to work through the entire firmware development cycle - from Pre-Si activities (simulation, emulation) to Post-Si activities (board bring-up).
Modeling of the hardware components is done in C++ using systemC, while the test environment is written in Python.

Qualifications:

- Master's degree student in Electrical / Computer Engineering with at least 3 semesters till graduation OR Bachelor's degree student in Electrical / Computer Engineering with 3-4 semesters till graduation.
- An ability to work at least 27 hours a week (3 full days in regular business hours) with a hybrid model.
- Good communication skills among groups inside and outside of Israel
- Fast learner

Additional qualifications would be an added advantage:

- Experience in firmware/hardware verification and validation
- Experience in the development of firmware/hardware development environment with code/functional coverage.
- Experience in firmware validation concepts and methodologies
- Experience in real-time/embedded firmware development and/or very-large-scale-integration (VLSI) hardware design
- Experience in OOP/OOD in C++ and/or Python
- Familiarity with CPU architecture.

Job Type:

Student / Intern

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