

**Like No Place You've
Ever Worked.**



Chip Design Student

Israel, Yokneam

NVIDIA is looking for a Chip-Design Verification Student to join our Network Adapter Silicon group.

As a Chip Design Student at NVIDIA Networking Silicon team, you will join a group of passionate engineers and students to design and implement the next generation state-of-the-art Networking Silicon chips. In this position, you will make a real impact in a dynamic, technology-focused company while developing the industry's best high-speed communication devices, delivering the highest throughput and lowest latency!

What you'll be doing:

- Work in a combined design and verification team which develops core units within the Networking silicon.
- Build reference models, verify and simulate chip blocks/entities according to specifications under challenging constraints with high orientation to performance.
- Work closely with multiple teams within organizations such as Architecture, Micro-Architecture, and FW.

What we need to see:

- Studies toward B.Sc. in Electrical Engineering or Computer Engineering.
- Completion of programming and logic design courses with high scores.
- A team player with good communication and interpersonal skills.

NVIDIA has some of the most forward-thinking and hardworking people in the world working for us. Are you creative and autonomous? Do you love the challenge of crafting the highest performance & lowest power silicon possible? If so, we want to hear from you. Come, join our Network Adapter Silicon design team and help us build the next chip in this exciting and quickly growing field.

Apply [here](#)

Physical Design Signoff CAD Student

Israel, Yokneam

NVIDIA is looking for best-in-class Physical Design CAD student to join our outstanding Networking Silicon engineering team, developing the industry's best high speed communication devices, delivering the highest throughput and lowest latency! Come and take a part in designing our groundbreaking and innovating chips, enjoy working in a meaningful, growing and highly professional environment where you make a significant impact in a technology-focused company.

What you'll be doing:

- You will be developing physical design, STA, Logic eq, Power Integrity flows and methodologies for implementation of networking chips and SOCs.
- Partner closely with block owners, full Chip STA engineers to assure high quality and timely convergence.
- Come up with unique and creative solutions to the state of the art physical design problems that are needed for Our chips.
- Additional responsibilities include participating and developing flow and tool methodologies for timing analysis and closure, power and noise analysis, IR-drop, EM and back-end verification across multiple projects.

What we need to see:

- B.SC./ M.SC. Student in Electrical Engineering/Computer Engineering.
- Self-motivation, attention to detail, and good written, verbal, and presentation skills are critical to success in this role.
- Ownership, self-learning skills, and ability to work autonomously

Ways to stand out from the crowd:

- Knowledge in Tcl/Perl/Python
- Versatile
- Great teammate

NVIDIA is widely considered to be one of the technology world's most desirable employers. We have some of the most forward-thinking people in the world working for us. Are you a creative and autonomous engineer who loves a challenge? Are you ready to become the engineer you always wanted to be? Come and be part of the best physical design team in the industry!

Apply: [here](#)